

Analog/Mixed Signal IC Design Engineer

Né(e) en 2000 · 75000 Paris · 0 à 1 an d'expérience · Réf. 2606291343

Ma recherche

Contrat recherché : Tout contrat

Zone : Ile de France

Mobilité : sur toute la région

Rémunération : a étudier

Secteur : Batiment

Formations

12/2025 — Bac +5 — Institut Polytechnique de Paris

Master 2 — Integration, Circuits and Systems Key topics: RF electronics, microelectronics, MEMS/NEMS, embedded systems, high performance ADC/DAC design, hardware signal processing.

12/2024 — Bac +5 — Institut Polytechnique de Paris

Master 1 — Electrical Engineering for Communications & Information Processing Focus on communications, electrical/optical engineering, microelectronics, and emerging device technologies. Coursework taught in English.

12/2023 — Bac +3 — University of Tabriz

Bachelor of Electrical Engineering Foundation in circuits, analog/digital electronics, semiconductors, control, and signal processing.

Expériences professionnelles

05/2025 - 09/2025 — Analog IC Design Intern

Designed a fully integrated low noise, low power chopper amplifier for neural signal acquisition in 65 /nm CMOS. Performed schematic design and simulations (AC, transient, noise, stability, PSS) and implemented chopping, ripple reduction, input impedance boosting and DC servo techniques in Cadence Virtuoso. Prepared documentation and reports detailing methodology and results.

Langues

Persian (maternelle), Azerbaijani (maternelle), English (maternelle), Turkish (maternelle), French (notions)

Atouts

Cadence Virtuoso, Spectre, SPICE, Verilog A, Analog/Mixed Signal Design, CMOS Layout, Low Power Circuits, Post Layout Simulations, AFE Design, Verilog, C programming, MATLAB, Python (basic), RF Electronics, Data Converters, Sensor Interfaces, Biomedical Circuits, Mixed Signal Systems, Low Noise Low Power Chopper Amplifier (project), Capacitive Isolation System for AC Switch Control (project), Indoor Positioning System (WiFi+INS Fusion) (project), UWB Positioning Research (project)

Loisirs

Analog IC design, Biomedical sensor interfaces, Ultra low power systems, RF front ends, Data converter architectures, Multicultural work environments

Fiche anonyme — l'identité et les coordonnées du candidat sont communiquées aux recruteurs qui en font la demande sur pmebtp.com.