

Né le 14/03/2000  
75000 Paris  
0 à 1 an d'expérience  
Réf : 2606291343

## Analog/mixed $\square$ signal ic design engineer

### Ma recherche

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Je recherche tout type de contrat, sur toute la région Ile de France, dans le Batiment.

### Formations

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**12/2025** : Bac +5 à Institut Polytechnique de Paris

Master 2 — Integration, Circuits and Systems Key topics: RF electronics, microelectronics, MEMS/NEMS, embedded systems, high  $\square$  performance ADC/DAC design, hardware signal processing.

**12/2024** : Bac +5 à Institut Polytechnique de Paris

Master 1 — Electrical Engineering for Communications & Information Processing Focus on communications, electrical/optical engineering, microelectronics, and emerging device technologies. Coursework taught in English.

**12/2023** : Bac +3 à University of Tabriz

Bachelor of Electrical Engineering Foundation in circuits, analog/digital electronics, semiconductors, control, and signal processing.

### Expériences professionnelles

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**05/2025 - 09/2025** :

Analog IC Design Intern chez Aarhus University sur Aarhus

Designed a fully integrated low  $\square$  noise, low  $\square$  power chopper amplifier for neural signal acquisition in 65 nm CMOS. Performed schematic design and simulations (AC, transient, noise, stability, PSS) and implemented chopping, ripple reduction, input  $\square$  impedance boosting and DC  $\square$  servo techniques in Cadence Virtuoso. Prepared documentation and reports detailing methodology and results.

### Langues

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Persian (Oral : maternelle / Ecrit : expérimenté), Azerbaijani (Oral : maternelle / Ecrit : expérimenté), English (Oral : maternelle / Ecrit : expérimenté), Turkish (Oral : maternelle / Ecrit : expérimenté), French (Oral : notions / Ecrit : débutant)

### Centres d'intérêts

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Analog IC design, Biomedical sensor interfaces, Ultra  $\square$  low  $\square$  power systems, RF front  $\square$  ends, Data converter architectures, Multicultural work environments